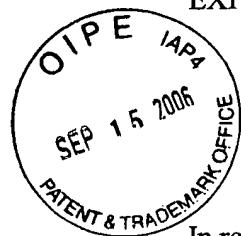


09-18-06

IFW / 78

EXPRESS MAIL LABEL NO.: EV 565664762 US

PATENT APPLICATION
Docket No.: 15436.253.66.1



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Hofmeister et al.

Serial No.: 10/695,342) Art Unit
Filed: October 28, 2003) 2828
Confirmation No.: 5604)
For: TEMPERATURE AND JITTER COMPENSATION)
CONTROLLER CIRCUIT AND METHOD FOR)
FIBER OPTICS DEVICE)
Examiner: Dung T. Nguyen)

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please find, pursuant to 37 C.F.R. § 1.98(a)(1), the enclosed Form PTO-1449 which contains a list of all patents, publications, or other items that have come to the attention of one or more of the individuals designated in 37 C.F.R. § 1.56(c). While no representation is made that any of these references may be "prior art" within the meaning of that term under 35 U.S.C. §§ 102 or 103, the enclosed list of references is disclosed so as to fully comply with the duty of disclosure set forth in 37 C.F.R. § 1.56.

Moreover, while no representation is made that a specific search of office files or patent office records has been conducted or that no better art exists, the undersigned attorney of record believes that the enclosed art is the closest to the claimed invention (taken in its entirety) of which the undersigned is

presently aware, and no art which is closer to the claimed invention (taken in its entirety) has been knowingly withheld.

In accordance with 37 C.F.R. §§ 1.97 and 1.98, a copy of each of the listed non-patent references or relevant portion thereof is also enclosed.

In accordance with 37 C.F.R. § 1.98(c), all English translations known by the undersigned to be within the possession, custody, control or availability of anyone designated in 37 C.F.R. § 1.56(c) of each non-English reference, if any, are also enclosed.

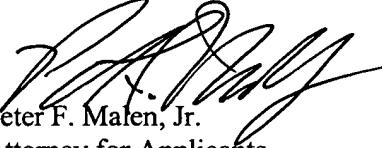
Since all listed references are either in the English language or are accompanied by a translation into English or an English language Abstract, no concise explanation of relevance is required under 37 C.F.R. § 1.98(a)(3).

Submission Fee
Under 37 C.F.R. § 1.97(c)

In accordance with 37 C.F.R. § 1.97(c), payment in the amount of \$180.00, to cover the submission fee, is enclosed to secure consideration of the references submitted with this Information Disclosure Statement. Please credit any over payment or charge any additional fees to Deposit Account No. 23-3178 of the undersigned.

Dated this 15th day of September, 2006.

Respectfully submitted,


Peter F. Malen, Jr.
Attorney for Applicants
Registration No. 45,576
Customer No. 022913
Telephone No. 801-533-9800

CERTIFICATE OF MAILING BY "EXPRESS MAIL" (37 CFR 1.10)Applicant(s): **Hofmeister et al.**

Docket No.

15436.253.66.1Application No.
10/695,342Filing Date
October 28, 2003Examiner
Dung T. NguyenCustomer No.
022913Group Art Unit
2828**INVENTOR: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT AND METHOD FOR FIBER OPTICS DEVICE****SEP 15 2006**
U.S. PATENT & TRADEMARK OFFICE

I hereby certify that the following correspondence:

Information Disclosure Statement (2 pgs); Form PTO-1449 (9 pgs); Transmittal Letter (2 pgs in duplicate); Copies of 39 references, Credit Card Payment Form; and postcard*(Identify type of correspondence)*

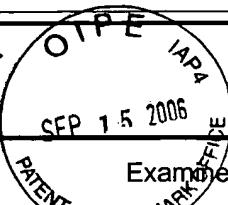
is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

September 15, 2006*(Date)***Gina Meredith***(Typed or Printed Name of Person Mailing Correspondence)**(Signature of Person Mailing Correspondence)***EV 565664762 US***("Express Mail" Mailing Label Number)***Note: Each paper must have its own certificate of mailing.**

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
15436.253.66.1

In Re Application Of: **Hofmeister et al.**



Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/695,342	October 28, 2003	Dong T. Nguyen	022913	2828	5604

Title: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT AND METHOD FOR FIBER OPTICS DEVICE

Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

37 CFR 1.97(b)

- The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

- The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

the statement specified in 37 CFR 1.97(e);

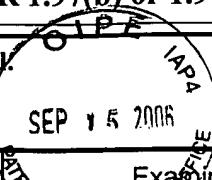
OR

the fee set forth in 37 CFR 1.17(p).

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
15436.253.66.1

In Re Application of: **Hofmeister et al.**



Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/695,342	October 28, 2003	Dung T. Nguyen	022913	2828	5604

Title: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT AND METHOD FOR FIBER OPTICS DEVICE

Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- A check in the amount of _____ is attached.
- The Director is hereby authorized to charge and credit Deposit Account No. 23-3178 as described below.
 - Charge the amount of _____
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 Attorney for Applicants
 Reg. No. 45,576
 Telephone No. 801-533-9800

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Dated: September 15, 2006

CC:

Applicant: Hofmeister, et al.

Serial No.: 10/695,342

Att'y Docket No.: 15436.253.66.1

Filing Date: October 28, 2003

Group: 2828

For:

TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT
AND METHOD FOR FIBER OPTICS DEVICEINFORMATION DISCLOSURE CITATIONS MADE BY APPLICANTU.S. Patent Documents

Examiner Initial*	Document Number	Issue Date	Name
____ 1	4,359,553	11/16/1982	Edwards
____ 2	4,378,451	03/29/1983	Edwards
____ 3	4,687,924	08/18/1987	Galvin et al.
____ 4	4,734,914	03/29/1988	Yoshikawa
____ 5	4,747,091	05/24/1988	Doi
____ 6	4,809,286	02/28/1989	Kollanyi et al.
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____ 8	4,932,038	06/05/1990	Windus
____ 9	5,019,769	05/28/1991	Levinson
____ 10	5,039,194	08/13/1991	Block et al.
____ 11	5,041,491	08/20/1991	Turke et al.
____ 12	5,268,949	12/07/1993	Watanabe et al.
____ 13	5,287,375	02/1994	Fujimoto
____ 14	5,334,826	08/02/1994	Sato et al.
____ 15	5,383,208	01/17/1995	Queniat et al.

Examiner:

Date Considered:

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant: Hofmeister, et al.

Serial No.: 10/695,342

Att'y Docket No.: 15436.253.66.1

Filing Date: October 28, 2003

Group: 2828

For:

TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT
AND METHOD FOR FIBER OPTICS DEVICE

_____ 16	5,392,273	02/21/1995	Masaki et al.
_____ 17	5,396,059	03/07/1995	Yeates
_____ 18	5,448,629	09/05/1995	Bosch et al.
_____ 19	5,516,563	05/14/1996	Schumann et al.
_____ 20	5,557,437	09/17/1996	Sakai et al.
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_____ 22	5,594,748	01/14/1997	Jabr
_____ 23	5,604,758	02/1997	AuYeung et al.
_____ 24	5,673,282	09/30/1997	Wurst
_____ 25	5,748,672	05/1998	Smith et al.
_____ 26	5,761,216	06/02/1998	Sotome et al.
_____ 27	5,801,866	09/01/1998	Chan et al.
_____ 28	5,812,572	09/22/1998	King et al.
_____ 29	5,854,704	12/29/1998	Grandpierre
_____ 30	5,926,303	07/20/1999	Giebel et al.
_____ 31	5,953,690	09/14/1999	Lemon et al.
_____ 32	5,956,168	09/21/1999	Levinson et al.
_____ 33	5,966,395	10/1999	Ikeda
_____ 34	6,055,252	04/2000	Zhang

Examiner:

Date Considered:

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Applicant: Hofmeister, et al.

Serial No.: 10/695,342

Att'y Docket No.: 15436.253.66.1

Filing Date: October 28, 2003

Group: 2828

For:

TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT
AND METHOD FOR FIBER OPTICS DEVICE

_____ 35	6,064,501	05/16/2000	Roberts et al.
_____ 36	6,157,022	12/05/2000	Meada et al.
_____ 37	6,160,647	12/12/2000	Gilliland et al.
_____ 38	6,175,434	01/2001	Feng
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_____ 41	6,205,505	03/20/2001	Jau et al.
_____ 42	6,222,660	04/24/2001	Traa
_____ 43	6,229,788	05/2001	Graves et al.
_____ 44	6,256,127	07/03/2001	Taylor
_____ 45	6,292,497	09/2001	Nakano
_____ 46	6,313,459	11/2001	Hoffe et al.
_____ 47	6,423,963	07/23/2002	Wu
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_____ 50	6,519,255	02/2003	Graves
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_____ 52	6,570,149	05/2003	Maruyama et al.
_____ 53	6,594,050	07/2003	Jannson et al.

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Filing Date: October 28, 2003

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For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT
AND METHOD FOR FIBER OPTICS DEVICE

<input type="checkbox"/> 54	6,631,146	10/07/2003	Pontis et al.
<input type="checkbox"/> 55	6,643,472	11/2003	Sakamoto et al.
<input type="checkbox"/> 56	6,661,836	12/2003	Dalal et al.
<input type="checkbox"/> 57	6,694,462	02/2004	Reis et al.
<input type="checkbox"/> 58	6,748,181	06/2004	Miki et al.
<input type="checkbox"/> 59	6,937,949	08/30/2005	Fishman et al.
<input type="checkbox"/> 60	6,941,077	09/06/2005	Aronson et al.
<input type="checkbox"/> 61	6,952,531	10/04/2005	Aronson et al.
<input type="checkbox"/> 62	7,020,567	03/28/2006	Fishman et al.
<input type="checkbox"/> 63	7,058,310	06/06/2006	Aronson et al.

U.S. Published Patent Application Documents

Examiner <u>Initial*</u>	Document <u>Number</u>	Pub. <u>Date</u>	<u>Name</u>
<input type="checkbox"/> 64	2001/0046242	11/29/2001	Kawakami et al.
<input type="checkbox"/> 65	2001/0046243	11/29/2001	Schie
<input type="checkbox"/> 66	2002/0021468	02/21/2002	Kato et al.
<input type="checkbox"/> 67	2002/0027688	03/07/2002	Stephenson
<input type="checkbox"/> 68	2002/0060824	05/23/2002	Liou et al.

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TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT
AND METHOD FOR FIBER OPTICS DEVICE

____ 69	2002/0097468	07/25/2002	Mecherle et al.
____ 70	2002/0101641	08/2002	Kurchuk
____ 71	2002/0105982	08/2002	Chin et al.
____ 72	2002/0129379	09/2002	Levinson et al.
____ 73	2002/0149821	10/17/2002	Aronson et al.
____ 74	2002/0181519	12/2002	Vilhelmsson et al.
____ 75	2002/0181894	12/05/2002	Gilliand et al.
____ 76	2003/0053170	03/2003	Levinson et al.
____ 77	2003/0110509	06/2003	Levinson et al.
____ 78	2003/0113118	06/19/2003	Bartur
____ 79	2003/0169790	09/11/2003	Chieng et al.
____ 80	2003/0210917	11/13/2003	Stewart et al.
____ 81	2004/0076113	04/22/2004	Aronson et al.
____ 82	2004/0120720	06/24/2004	Chang et al.
____ 83	2004/0153913	08/05/2004	Fishman et al.
____ 84	2004/0202210	10/14/2004	Thornton
____ 85	2004/0240886	12/02/2004	Aronson et al.
____ 86	2004/0253003	12/2004	Farmer et al.
____ 87	2005/0031352	02/10/2005	Light et al.

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Date Considered:

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For:

TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT
AND METHOD FOR FIBER OPTICS DEVICE 88 2005/0058455 03/17/2005 Hosking et al.Foreign Patent Documents

<u>Examiner Initial*</u>	<u>Document Number</u>	<u>Publication Date</u>	<u>Country or Patent Office</u>
<u> </u> 89	JP 402102589 A	04/16/1990	Japan
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<u> </u> 93	WO 98/00893	01/08/1998	PCT
<u> </u> 94	WO 98/00943	08/01/1998	PCT
<u> </u> 95	EP0745868B1	04/17/2002	EPO
<u> </u> 96	PCT/US02/03226	05/09/2002	PCT (search report)
<u> </u> 97	WO 02/063800 A1	08/15/2002	PCT
<u> </u> 98	EP 02704344	10/05/2004	EPO (search report)
<u> </u> 99	EP 04017254	10/05/2004	EPO (search report)
<u> </u> 100	WO/2004/098100	11/11/2004	PCT
<u> </u> 101	PCT/US04/11130	10/12/2004	Search Report
<u> </u> 102	EP 1471671 A2	12/27/2004	EPO

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For:

TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT
AND METHOD FOR FIBER OPTICS DEVICE

<input type="checkbox"/> 103	JP 58140175 A (abstract)	08/19/1983	Japan
<input type="checkbox"/> 104	JP 62124576 A (abstract)	06/05/1987	Japan
<input type="checkbox"/> 105	JP 62235975 A (abstract)	10/16/1987	Japan
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(including author, title, pertinent pages, etc.)

Examiner
Initial*

107 Yi Cai et al., "Jitter testing for gigabit serial communication transceivers," Jan – Feb 2002, IEEE Design and Test of Computers, Vol. 19, Issue 1, pp 66-74.

108 MAEDA, Noriyuki "Notification of Reason(s) for Refusal," Japanese Patent Application No. JP2002-563630, Nakamura, M. et al., July 13, 2005.

109 Finisar Corp., "App. Note AN-2025: Using the Finisar GBIC I²C Test Diagnostics Port," 1998.

110 Hausdorf, Reiner, "Mobile Transceiver Measurements with Radiocommunication Service Monitor CMS," News from Rohde & Schwarz, 127, IV, 1989, pp 4-7.

111 Webopedia: The 7 Layers of the OSI Model [online] [retrieved 10/15/03]. Retrieved from Internet: URL: http://webopedia.internet.com/quick_ref/OSI_Layers.asp

112 Webopedia.com: Public-Key Encryption [online] [retrieved 10/15/03]. Retrieved from Internet: URL: http://www.webopedia.com/TERM/p/public_key_cryptography.html

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Applicant: Hofmeister, et al.
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For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT
AND METHOD FOR FIBER OPTICS DEVICE

Att'y Docket No.: 15436.253.66.1
Group: 2828

- ____ 113 Webopedia.com: MAC Address [online] [retrieved 10/15/03]. Retrieved from Internet: URL: http://www.webopedia.com/TERM/M/MAC_address.html
- ____ 114 Webopedia.com: 12C [online] [retrieved 11/11/03]. Retrieved from Internet: URL: <http://www.webopedia.com/TERM/I/12C.html>
- ____ 115 Manchester Encoding [online] [retrieved 11/12/03]. Retrieved from Internet: URL: <http://www.erg.abdn.ac.uk/users/gorry/course/phy-pages/man.html>
- ____ 116 Documentation entitled "IR Receiver ASSP: T2525", copyright 2003 by Atmel Corporation
- ____ 117 Documentation entitled "IR Receiver for Data Communication: U2538B", copyright 2003 by Atmel Corporation
- ____ 118 Documentation entitled "Low-Voltage Highly Selective IR Receiver IC: T2527", copyright 2002 by Atmel Corporation
- ____ 119 Documentation entitled "Application Note: T2525/26/27", copyright 2003 by Atmel Corporation
- ____ 120 *LXT16706/16707 SerDes Chipset*, Intel Products,
www.intel.com/design/network/products/optical/phys/lxt16706.htm,
April 19, 2002.
- ____ 121 *LXT35401 XAUI-to-Quad 3.2G Transceiver*, Intel Products,
www.intel.com/design/network/products/optical/phys/lxt35401.htm,
April 19, 2002
- ____ 122 Texas Instruments User's Guide, *TLK2201 Serdes EVM Kit Setup and Usage*,
Mixed Signal DSP Solutions, July 2000.
- ____ 123 Texas Instruments User's Guide, *TLK1501 Serdes EVM Kit Setup and Usage*,
Mixed Signal Products, June 2000.

Examiner:

Date Considered:

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Filing Date: October 28, 2003

Group: 2828

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TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT
AND METHOD FOR FIBER OPTICS DEVICE

124 National Semiconductor DS92LV16 Design Guide, *Serializing Made Simple*, February 2002.

125 Vaishali Semiconductor, *Fibre Channel Transceiver*, VN16117, MDSN-0002-02, 08/09/2001.

126 Fairchild Semiconductor, Application Note 77, *CMOS, the Ideal Logic Family*, January 1983.

127 Analog Target Specification, Annex 48B, Published by IEEE New York, 05/2001, pp. 6-14.

References Cited by Applicants

While the filing of Information Disclosure Statements is voluntary, the procedure is governed by the guidelines of Section 609 of the Manual of Patent Examining Procedure and 37 C.F.R. §§ 1.97 and 1.98. To be considered a proper Information Disclosure Statement, Form PTO-1449 shall be accompanied by a copy of each listed patent or publication or other item of information and a translation of the pertinent portions of foreign documents (if an existing translation is readily available to the applicant), an explanation of relevance of each reference not in the English language, and should be submitted in a timely manner as set out in MPEP Sec. 609.

Examiners will consider all citations submitted in conformance with 37 C.F.R. § 1.98 and MPEP Sec. 609 and place their initials adjacent the citations in the spaces provided on this form. Examiners will also initial citations not in conformance with the guidelines which may have been considered. A reference may be considered by the Examiner for any reason whether or not the citation is in full conformance with the guidelines. A line will be drawn through a citation if it is not in conformance with the guidelines AND has not been considered. A copy of the submitted form, as reviewed by the Examiner, will be returned to the applicant with the next communication. The original of the form will be entered into the application file.

Each citation initialed by the Examiner will be printed on the issued patent in the same manner as references cited by the Examiner on Form PTO-892.

The reference designations "A1," "A2," etc. (referring to Applicant's reference 1, Applicant's reference 2, etc.) will be used by the Examiner in the same manner as Examiner's reference designations "A," "B," "C," etc. on Office Action Form PTO-1142.

GPM0000002495V001

Examiner:

Date Considered:

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.